

IN THE SPECIFICATION:

Please replace paragraph number 0010 with the following:

[0010] In conventional “double-down stud” gaming devices and methods, as disclosed in U.S. Patents 5,820,460, 5,411,257, 5,251,897, and 5,167,413, a hand of five cards is dealt, with the faces of one or more of the cards not being shown to the player. Based on the cards that are made visible to the player and prior to making the remaining cards visible to the player, the player may choose to wager an additional amount, thereby increasing the amount of winnings that are available to the player.

Please replace paragraph number 0015 with the following:

[0015] The present invention includes a gaming method providing a primary game in conjunction with one or more bonus games, as well as apparatus for effecting the method. In the primary game, a player selects, from an array of symbols to be randomly generated, one or more paylines along which the game will be played. The array of symbols is then generated and displayed. If a trigger symbol is present along one or more of the selected paylines, one or more bonus games may be initiated. In addition, the presence of a trigger symbol in a specified location within the array, such as at the center, may cause another bonus event to occur. For example, a part of the payout or the total payout of the game may be increased as a result of the additional bonus event.

Please replace paragraph number 0024 with the following:

[0024] FIG. 4 is a schematic representation of a primary game of an exemplary, poker embodiment of the method of the invention, wherein the primary game is stud poker;

Please replace paragraph number 0040 with the following:

[0040] When the primary game is stud poker, once a “hand” of cards 36' have been dealt, each selected payline 35 is examined to determine whether the combination of cards 36'

thereon comprises a winning hand. For example, certain known combinations of winning cards, including, without limitation, an ordered royal flush (a type of winning poker hand unique to gaming devices), a royal flush, a straight flush, four-of-a-kind, a full house, a flush, a straight, three-of-a-kind, two pair, and certain pairs of cards, may comprise a winning hand. The amount of award provided to the player for receiving a winning hand on a particular selected payline 35 preferably corresponds to the combination of cards 36' along that selected payline 35, and may be determined by referring to a pay table 39, such as that depicted in FIG. 4, suitable for the type of primary game being played.

Please replace paragraph number 0043 with the following:

[0043] When play proceeds to a bonus round, as shown in FIG. 5, a bonus game is played. In the example depicted in FIG. 5, the bonus game is draw poker, with cards 36' of the original, dealt draw poker hand on bonus payline 38' being the same cards 36' as those that were present in a corresponding selected payline 35 from the primary game. As shown in FIGs. 5 and 6, the player may then decide which cards 36H' to hold, while discarding the remaining cards 36D', which may then be randomly replaced with other, previously undealt cards 36R' from the same deck or from a "fresh" deck which may include the same number and types of cards 36' as the deck used in the primary game. Again, the amount of the award or award increase provided to the player based on the results of each bonus game may correspond to the final combination of cards 36' present in the bonus game, and may be determined by referring to a pay table 39 associated with the type of bonus game being played. The same or different pay tables 39 may be used for the primary and bonus games.

Please replace paragraph number 0048 with the following:

[0048] An example of an electronic gaming device 40 in which the poker embodiment of the method of the present invention may be effected is illustrated in FIG. 7. Gaming device 40 includes one or more processors 42 under control of programming (e.g., software or firmware)

that effects the method disclosed herein with reference to FIGs. 4-6. While a single processor 42 may be used in gaming device 40, different processors 42 may be used, for example, to effect the primary game and the bonus game. Each processor 42 communicates, as known in the art, by way of signals transported along carrier waves, with memory 43 of a known type, including read-only memory (ROM), random access memory (RAM), a hard drive, or CD-ROM, a display 44 of a known type (e.g., a cathode ray tube (CRT) monitor, a plasma monitor, a liquid crystal display, or other flat panel display, etc.), input elements 46 of a known type, such as the illustrated buttons or touch-sensitive locations on display 44, as well as with other known types of output devices, such as sirens, bells, whistles, other alarms, and audio devices. Data are input into and output from each processor 42 of gaming device 40 in accordance with the method of the present invention. Of course, gaming device 40 may also include one or more wagering input components 48 of known types, such as a currency receptacle, a credit or debit card reader, or a smart card reader, to permit a player to wager money on the games being played, as well as one or more payout components 49, as known in the art. The various elements of gaming device 40 and their association with each processor 42 and with one another are well known in the art.

Please replace paragraph number 0059 with the following:

[0059] The programming that controls processor 52 causes various logic circuits or groups of logic circuits of processor 52 to execute various functions, or elements of the inventive method. In the exemplary reel type embodiment of the inventive method, which is described herein with reference to FIGs. 8 and 9, one logic circuit of processor 52 (FIG. 10) may identify selected paylines 35" as a wager of the money credits or tournament points indicated by memory 53 (FIG. 7) to be available is placed thereon. Another logic circuit may randomly assign symbols 36" of a predetermined set to image sites 32" of array 30" or cause adjacent reels with series of symbols 36" thereon to spin and stop in random positions, with the shown, adjacent symbols 36" of each reel forming a column of symbols 36" of array 30". Yet another logic circuit of processor 52 may determine whether a winning combination of symbols 36" is

present along any selected payline 35" and, if so, determine the corresponding award, while another logic circuit may cause the amount of credit stored in memory 53 to be increased accordingly. Still another logic circuit may determine whether a bonus triggering event has occurred, either in or along a selected payline 35" or at a trigger site 32T". Logic circuits of processor 52, under control of appropriate programming, may also effect the appropriate bonus events, including both bonus games and increases in awards, as well as other elements of the inventive methods.

Please replace paragraph number 0060 with the following:

[0060] The apparatus illustrated in FIGs. 7 and 10 may comprise stand-alone gaming devices 40, 50 or stand-alone computers, or may (see FIG. 11) be networked to a central computer 60 (e.g., a server or game controlling computer) of a casino, or directly to other gaming devices 40, 50, by way of a network card 61 and appropriate links 62 therefor (i.e., a local area network (LAN)), by use of a modem or other communication component 63 that facilitates the use of broadband telecommunications between gaming devices 40, 50 or of gaming devices 40, 50 with a central computer 60 (i.e., a wide area network (WAN) or the World-Wide Web (WWW)), or otherwise, as known in the art. Accordingly, gaming devices 40, 50 may be linked to a central computer 60 and/or other gaming devices 40, 50 that are located at the same site or at remote sites (e.g., in other casinos or even other geographic locations). Exemplary ways in which gaming devices 40, 50 may be linked to one another are described in co-pending U.S. Application Ser. No. 09/675,829, filed on September 29, 2000 (which discloses linking gaming devices for the purpose of tournament play), and in U.S. Patent 6,039,648, the disclosures of each of which are hereby incorporated in their entireties by this reference.